

Biographical Sketch



Tadahiko Murata (IEEE Fellow) received his B.S., M.S., and Ph. D degrees from Osaka Prefecture University, Osaka, Japan in 1994, 1996, and 1997, respectively. He joined the Department of Industrial Engineering in Ashikaga Institute of Technology in 1997, then moved to Department of Informatics in Kansai University in 2001. He became a full professor at Kansai University in 2009. He then moved to Cybermedia Center (CMC), The University of Osaka in 2023 as a full professor. Since October 1, 2024, CMC has been reorganized as D3 (D-Three) Center that stands for Digital Design, Datability, and Decision intelligence Center conducting Data Science and Digital Infrastructure such as a supercomputing and networking not only for university researchers but also for public usages. He is giving classes at Graduate School of Information Science and Technology, and Undergraduate School of Engineering. His interests include multi-objective optimization, social simulations, digital twin, and high-performance computing.

He was a Chair of IEEE SMC Society Japan Chapter from 2008 to 2011, a member of Board of Governors of IEEE SMC Society from 2015 to 2020, a President of Japanese Society for Evolutionary Computation from 2020 to 2022, and a Vice President for Organization and Planning, IEEE SMC Society from 2022 to 2025, and a Vice President of Japan Society for Fuzzy Theory and Intelligent Informatics from 2023 to 2026.