

Biographical Sketch



Tadahiko Murata (IEEE Fellow) received his B.S., M.S., and Ph. D degrees from Osaka Prefecture University, Osaka, Japan in 1994, 1996, and 1997, respectively. He joined the Department of Industrial Engineering in Ashikaga Institute of Technology in 1997, then moved to Department of Informatics in Kansai University in 2001. He became a full professor at Kansai University in 2009. He then moved to Osaka

University in 2023 as a full professor. In Osaka University, he is working for Cybermedia Center that is a supercomputing center not only for university researchers but also for public usages. He is giving classes at Graduate School of Information Science and Technology, and Undergraduate School of Engineering. His interests include multi-objective optimization, social simulations, digital twin, and high-performance computing.

He was a member of Board of Governors in IEEE SMC Society from 2015 to 2020, President of Japanese Society for Evolutionary Computation from 2020 to 2022, currently Vice President for Organization and Planning, IEEE SMCS from 2022 to 2025, and Vice President of Japan Society for Fuzzy Theory and Intelligent Informatics from 2023 to 2026.