

Senior Engineer in System Design for Machine Learning Applications Fraunhofer IIS, Erlangen, Germany.

JOB DETAILS

Title Senior Engineer – System Design for Machine Learning Applications

Employer Fraunhofer IIS, Erlangen

Job location Am Wolfsmantel 33, 91058 Erlangen

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Application deadline Unspecified

Job types Researcher, Engineer, Research assistant

Fields Informatics, Algorithms, Acoustics, Communication Engineering, Artificial Intelligence, Computing in Mathematics, Natural Science, Engineering and Medicine, Programming Languages, Software Engineering, Audio Systems Engineerin

You love technology and human interaction? You aim to combine innovative research and training to help companies in using latest machine learning findings?

Then join our team to found a lab on artificial intelligence for signal processing.

Leveraging the potential of machine learning for signal processing applications is only possible when the networks can be efficiently trained and executed on various platforms such as TPUs, GPUs, CPUs and FPGAs. To solve these challenges, you will evaluate state-of-the-art system design tools that help to create efficient implementations from trained machine learning networks. Moreover, you will develop novel methodologies and tools for training data creation. The collected know-how will form the base for professional seminars that you will elaborate and hold.

About us

The Fraunhofer IIS »Audio and Media Technologies« division has been making its mark on media digitalization for over 30 years, developing pioneering standards such as MP3 and AAC. Motivated by this success, we aim to address new challenges in artificial intelligence and machine learning by tremendously simplifying the design of efficient PC-based or embedded implementations.

What we expect from you

A scientific degree (Master, Ph.D. or equivalent) in Electronics, Computer Science, Natural Sciences, Engineering Sciences, or a related subject

Solid practical experiences in implementing machine learning algorithms on different platforms (GPU, TPU or FPGA)

Fundamental understanding of system design or compiler technologies

Enthusiasm in teaching or training

Sound command of English for working in an international team

Fluent German desirable

What you can expect from us

We offer you varied and challenging work in an enthusiastic team. You can contribute with your ideas and help design new concepts. Regular training is given, as are flexible, family-friendly work arrangements such as part-time contracts. You will have a state-of-the-art workstation in a modern, bright 2- to 3-person office.

Appointment, remuneration and social security benefits base on the public-sector collective wage agreement (TVöD).

In case of identical qualifications, preference will be given to severely disabled candidates. We would like to point out that the chosen job title also includes the third gender. The Fraunhofer-Gesellschaft emphasizes gender-independent professional equality.

Fraunhofer is Europe's largest application-oriented research organization. Our research efforts are geared entirely to people's needs: health, security, communication, energy and the environment. As a result, the work undertaken by our researchers and developers has a significant impact on people's lives. We are creative. We shape technology. We design products. We improve methods and techniques. We open up new vistas. Intimidating? Don't worry. We are an interdisciplinary team, looking to support young career-oriented talents.

Hence, do not hesitate to join the home of MP3 and send us your application (English or German)! Please submit it (in PDF format, including cover letter, CV and references) to Claudia Kestler-Böhm via our career portal, quoting reference number IIS-2019-AME107, under:
recruiting.fraunhofer.de/Vacancies/47468/Description/2

Information about the Institute is available online at:
iis.fraunhofer.de